



Molecular Crystals and Liquid Crystals

Publication details, including instructions for authors and subscription information:

<http://www.tandfonline.com/loi/gmcl20>

On the Correlation between Morphology and Electronic Properties of Fluorinated Copper Phthalocyanine ($F_{16}CuPc$) Thin Films

Rongbin Ye^a, Mamoru Baba^b, Yoshiyuki Ohishi^b,
Kunio Mori^b & Kazunori Suzuki^c

^a Iwate Industrial Promotion Center, Iioka-shinden,
Morioka, Japan

^b Faculty of Engineering, Iwate University, Ueda,
Morioka, Japan

^c Iwate Industrial Research Institute, Iioka-shinden,
Morioka, Japan

Version of record first published: 20 Aug 2006

To cite this article: Rongbin Ye, Mamoru Baba, Yoshiyuki Ohishi, Kunio Mori & Kazunori Suzuki (2006): On the Correlation between Morphology and Electronic Properties of Fluorinated Copper Phthalocyanine ($F_{16}CuPc$) Thin Films, Molecular Crystals and Liquid Crystals, 444:1, 203-210

To link to this article: <http://dx.doi.org/10.1080/15421400500364972>

PLEASE SCROLL DOWN FOR ARTICLE

Full terms and conditions of use: <http://www.tandfonline.com/page/terms-and-conditions>

This article may be used for research, teaching, and private study purposes. Any substantial or systematic reproduction, redistribution, reselling, loan, sub-licensing, systematic supply, or distribution in any form to anyone is expressly forbidden.

The publisher does not give any warranty express or implied or make any representation that the contents will be complete or accurate or up to date. The accuracy of any instructions, formulae, and drug doses should be independently verified with primary sources. The publisher shall not be liable for any loss, actions, claims, proceedings, demand, or costs or damages whatsoever or howsoever caused arising directly or indirectly in connection with or arising out of the use of this material.

On the Correlation between Morphology and Electronic Properties of Fluorinated Copper Phthalocyanine ($F_{16}CuPc$) Thin Films

Rongbin Ye

Iwate Industrial Promotion Center, Iioka-shinden, Morioka, Japan

Mamoru Baba

Yoshiyuki Ohishi

Kunio Mori

Faculty of Engineering, Iwate University, Ueda, Morioka, Japan

Kazunori Suzuki

Iwate Industrial Research Institute, Iioka-shinden, Morioka, Japan

We report on the correlation between morphology and electronic properties of fluorinated copper phthalocyanine ($F_{16}CuPc$) thin films deposited on SiO_2/Si substrates at different substrate temperatures. Highly ordered films with the 200 plane spacing of $d_{200} = 14.3 \text{ \AA}$ is observed. Increasing the substrate temperature significantly improves the molecular ordering of $F_{16}CuPc$, and the smallest FWHM was gained at a substrate temperature of 120°C . The mobility is strongly dependent on the substrate temperature. Increasing in grain size at higher substrate temperatures improves the mobility of $F_{16}CuPc$ TFTs, but gaps generated between grains degrade the performance of $F_{16}CuPc$ TFTs at a substrate temperature higher than 110°C . When deposition of $F_{16}CuPc$ is done at a substrate temperature of 100°C , the maximum mobility of $4.25 \times 10^{-3} \text{ cm}^2/\text{Vs}$. can be obtained.

Keywords: $F_{16}CuPc$; field-effect mobility; morphology; organic thin film transistor

The authors would like to thank Mr. K. Ohta and E. Miyamoto for their technical assistances. We also acknowledge the support of the Japan Science and Technology Corporation under the joint research project Collaboration of Regional Entities for the Advancement of Technological Excellence in Iwate Prefecture.

Address correspondence to Rongbin Ye, Iwate Industrial Promotion Center, 3-35-2, Iioka-shinden, Morioka 020-0852, Japan. E-mail: ye@dragon.elc.iwate-u.ac.jp

1. INTRODUCTION

Organic semiconductors have been widely studied over the two last decades for their potential applications as thin film transistors (TFTs), light-emitting diodes (LEDs), and photovoltaic cells [1–5]. Among a variety of materials, phthalocyanines (Pcs) with high thermal and chemical stability represent one of the most promising candidates for modern opto-electronic devices such as optical recording, organic LEDs, gas sensors, TFTs and solar cells [6–13]. Organic TFTs made from fluorinated copper phthalocyanine ($F_{16}CuPc$) led to an air-stable electron mobility of $0.03 \text{ cm}^2/\text{Vs}$, which is a promising material for n-channel operation [11]. On the other hand, the morphology and structure affect the performance of $F_{16}CuPc$ TFTs, which are very quite sensitive to growth conditions such as substrate temperature, deposition rate and type of the substrate [11,14–17]. Therefore, it is very important to control the growth parameters during growth of organic thin films.

In this work, we report on the correlation between morphology and electronic properties of $F_{16}CuPc$ thin film by X-ray diffraction (XRD), atomic force microscopy (AFM) and field-effect measurements. We also study the temperature dependence of the field-effect mobility for an $F_{16}CuPc$ TFT.

2. EXPERIMENTAL

$F_{16}CuPc$ (Fig. 1a) was purchased from Aldrich Chemical Co. and purified by sublimation twice at pressures of $3 \times 10^{-2} \text{ Pa}$ or lower. The schematic structure of the devices is shown in Figure 1b. A heavily n-doped Si substrate acts as the gate electrode with a 300 nm thermally grown SiO_2 layer ($C_i \sim 10 \text{ nF/cm}^2$) as the gate dielectric.

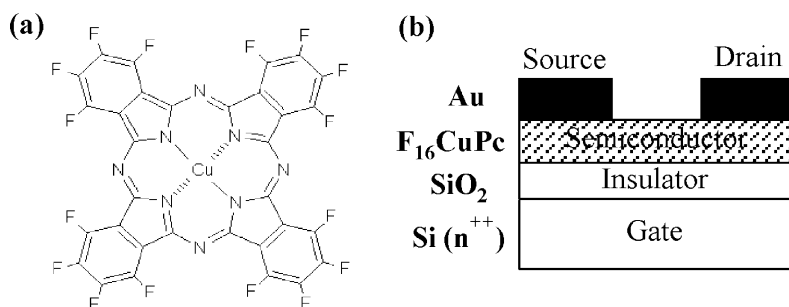


FIGURE 1 (a) Chemical structure of $F_{16}CuPc$ and (b) a schematic diagram of an $F_{16}CuPc$ -based OTFT.

$F_{16}CuPc$ thin films were deposited at various substrate temperatures (75, 100, 125 and 150°C, respectively) under a base pressure of less than 5×10^{-4} Pa. The substrate temperature was controlled by Digital Programming Regulator (KP1000, CHINO) with overshoot value less than 1°C and accuracy of $\pm 0.2^\circ\text{C}$. Film thicknesses (ca. 30 nm) and growth rates ($\sim 0.2 \text{ \AA/s}$) were monitored by a thickness and rate monitor (CRTM-6000, ULVAC). Finally, Au source and drain electrodes of ca. 100 nm were vacuum-deposited through a shadow mask with a channel width of 5 mm and lengths of from 50 to 70 μm . The characteristics of OTFTs were measured with a two-channel voltage current source/monitor system (R6245, ADVANTEST) under ambient laboratory air conditions. Temperature-dependence measurements were done by setting the device in a cryostat, which was temperature-controlled from 8.8 K to RT by a cryocooler using a He-gas flowing method.

The XRD analysis was performed on a diffractometer (Rint 2200 V, RIGAKU Co., Ltd.) with graphite monochromatized CuK_α radiation ($\lambda = 1.54 \text{ \AA}$). The morphology of the films was examined using the contact mode AFM (SPA 500, Seiko Instruments Co., Ltd.). The grain size

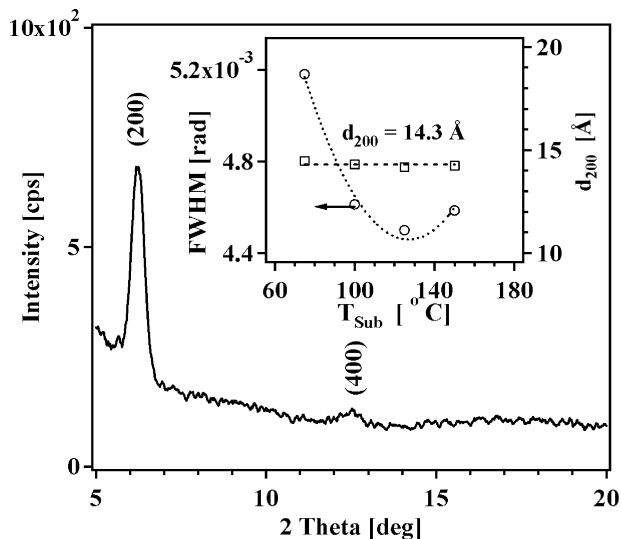


FIGURE 2 The XRD pattern of $F_{16}CuPc$ thin films deposited at 125°C. The inset shows the effect of the substrate temperature on full width of half maximum (FWHM) of the (200) diffraction lines from $F_{16}CuPc$ thin films deposited at 75, 100, 125 and 150°C.

and the RMS roughness were obtained by using AFM for each individual scan ($2\text{ }\mu\text{m} \times 2\text{ }\mu\text{m}$).

3. RESULTS AND DISCUSSION

Figure 2 shows XRD spectra of F_{16}CuPc thin films deposited at a substrate temperature of 125°C . Only α -form F_{16}CuPc with the 200 plane spacing $d_{200} = 14.3\text{ }\text{\AA}$ (seen in the inset of Fig. 2) is observed [11]. The two XRD peaks correspond to (200) and (400) reflections. This is the signature of highly ordered films and the trace of the herringbone pattern parallel to the substrate. On the other hand, the inset of Figure 2

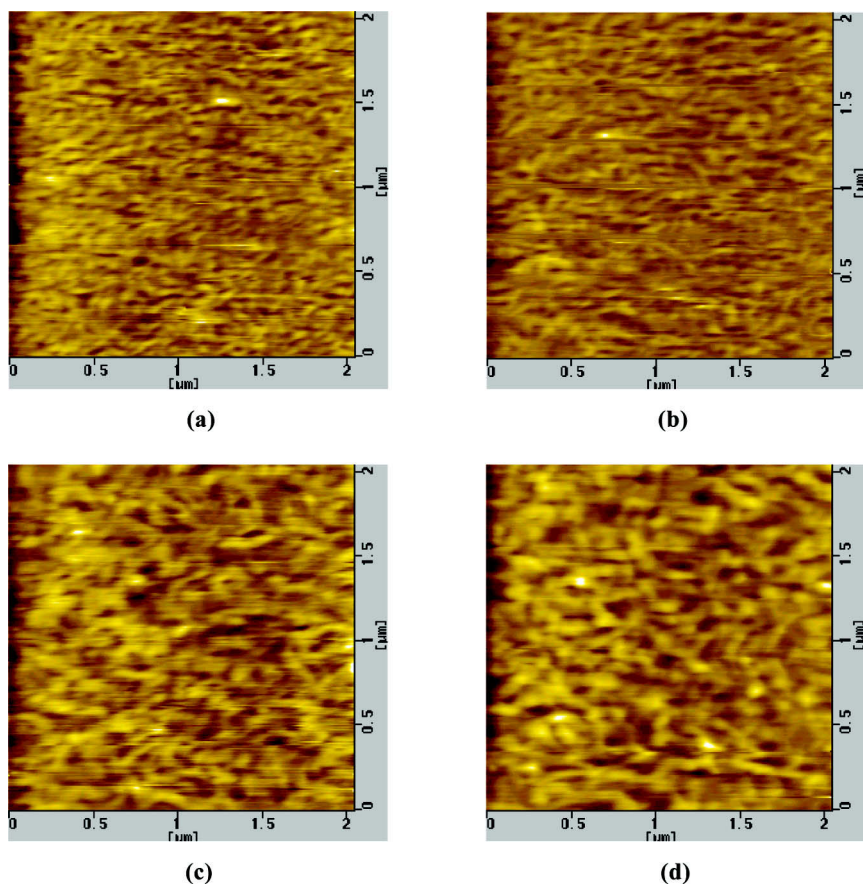


FIGURE 3 AFM images of F_{16}CuPc thin films deposited at (a) 75, (b) 100, (c) 125 and (d) 150°C .

shows the full width at half maximum (FWHM) of the (200) diffraction line as a function of the substrate temperature (T_{Sub}). Increasing the substrate temperature significantly improves the molecular ordering of $F_{16}CuPc$ and enhances crystallinity within the thin films, and the smallest FWHM was gained at $T_{Sub} = 120^{\circ}C$.

The morphology of $F_{16}CuPc$ films plays a critical role in determining their macroscopic semiconducting performance. Figure 3 shows the $2\mu m \times 2\mu m$ AFM topographic images of $F_{16}CuPc$ thin films deposited at 75, 100, 125 and $150^{\circ}C$. For these $F_{16}CuPc$ thin films, the growth of elongated bent strips that are lying parallel to the substrate surface can be observed. The length of the strips was evidently increasing for the higher substrate temperatures. As shown in Figure 4, the grain size and RMS increase with increasing the substrate temperature. The RMS roughness amplitude (σ) increases closely following an Arrhenius behavior as $\sigma \propto \exp(-E_{Bar}/k_B T)$ with the activation energy $E_{Bar} = 0.060$ eV, and k_B is the Boltzmann constant in the inset of Figure 4 [18].

In Figure 5, the drain current is plotted as a function of the gate voltage under a drain voltage of 60 V for $F_{16}CuPc$ TFTs deposited at

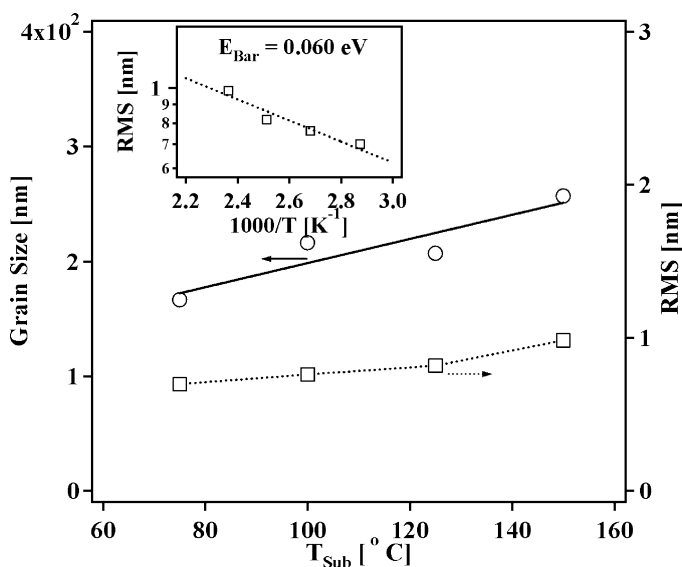


FIGURE 4 The RMS and grain size are shown as a function of substrate temperature of $F_{16}CuPc$ thin films. The inset shows the RMS amplitude (σ) vs. inverse temperature ($1000/T$) with a linear fit yielding the activation barrier of $E_{Bar} = 0.018$ eV.

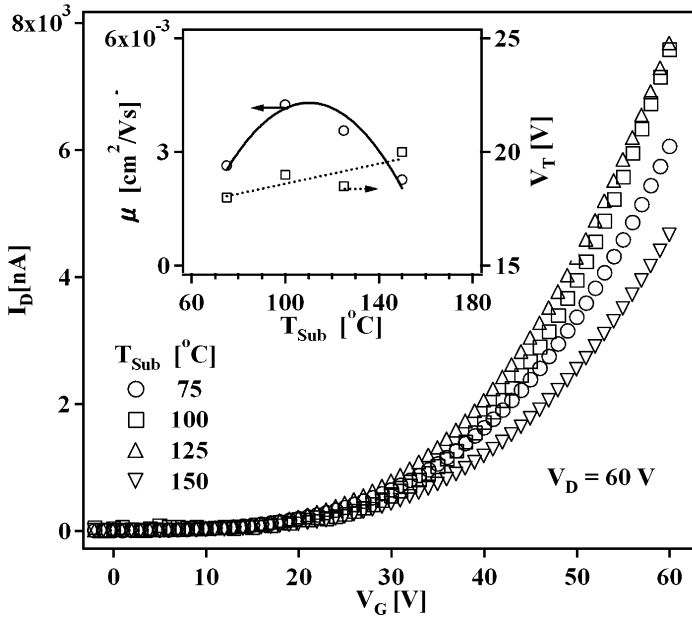


FIGURE 5 The drain current is plotted as a function of the gate voltage under a drain voltage of 60 V for $F_{16}CuPc$ TFTs deposited at 75, 100, 125 and 150°C. The inset shows the effects of substrate temperature on mobility and threshold voltage.

different substrate temperatures. The field-effect mobility (μ) and threshold voltage (V_T) of these OTFTs can be estimated using a standard analytic theory of MOSFET [19]. As shown in the inset of Figure 5, the mobility is strongly dependent on the substrate temperatures. Increasing grain size at high substrate temperatures improves electrical transport characteristics. On the other hand, gaps generated between grains degrade mobility of OTFTs at a substrate temperature higher than 110°C. When deposition of $F_{16}CuPc$ is done at a substrate temperature of 100°C, the maximum mobility of $4.25 \times 10^{-3} \text{ cm}^2/\text{Vs}$ can be obtained.

Furthermore, Figure 6 shows an Arrhenius plot of the temperature dependence mobility for the $F_{16}CuPc$ TFT deposited at 125°C. At high temperatures ($>140 \text{ K}$), the mobility is clearly thermally activated with thermal activation energy of $\Delta E = 0.039 \text{ eV}$. At temperatures ranging from 50 K to 140 K, we have a second region with lower activation energy of $\Delta E = 0.010 \text{ eV}$. Finally, for temperatures lower than 50 K, the mobility becomes practically temperature independent.

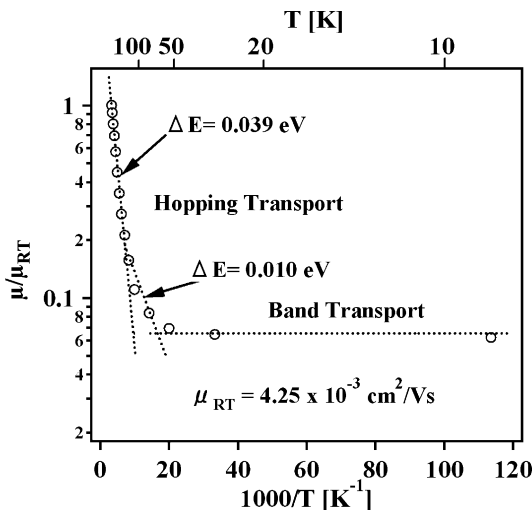


FIGURE 6 An Arrhenius plot of the temperature dependent mobility for the $F_{16}CuPc$ TFT deposited at $125^{\circ}C$.

These data could be explained by a mode where charge transport is limited by a high concentration at grain boundaries [20].

4. CONCLUSIONS

We have described a detailed characterization of the morphology and electrical properties of $F_{16}CuPc$ thin films deposited on SiO_2/Si substrates at different substrate temperatures. Highly ordered films with the 200 plane spacing of $d_{200} = 14.3 \text{ \AA}$ is observed. The substrate temperature significantly affects the $F_{16}CuPc$ molecular ordering, and the smallest FWHM was gained at T_{Sub} of $120^{\circ}C$. The mobility is strongly dependent on the substrate temperature. Increasing in grain size at higher substrate temperatures improves electrical transport characteristics. On the other hand, gaps generated between grains degrade the mobility of $F_{16}CuPc$ TFTs at a substrate temperature higher than $110^{\circ}C$. When deposition of $F_{16}CuPc$ is done at a substrate temperature of $100^{\circ}C$, the maximum mobility of $4.25 \times 10^{-3} \text{ cm}^2/Vs$ can be obtained.

REFERENCES

- [1] Torsi, L., Cioffi, N., Di Franco, C., Sabbatini, L., Zambonin, P. G., & Bleve-Zacheo, T. (2001). *Solid-State Electron*, 45, 1479–1485.

- [2] Garnier, F. (1998). *Chem. Phys.*, 227, 253–262.
- [3] Mitschke, U. & Bauerle, P. (2000). *J. Mater. Chem.*, 10, 1471–1507.
- [4] Dimitrakopoulos, C. D. & Mascaro, D. J. (2001). *IBM J. RES. & DEV.*, 45, 11–27.
- [5] Petritsch, K. & Friend, W. R. H. (1998). *Synth. Met.*, 102, 976.
- [6] Guillaud, G., Simon, J., & Germain, J. P. (1998). *Corrdination Chem. Rev.*, 1433, 178–180.
- [7] Kubo, K., Takiguchi, T., Okada, Y., Yoshida, Y., Tanigaki, N., & Yase, K. (1999). *J. Cryst. Growth*, 204, 248.
- [8] Ottmar, M., Hohnholz, D., Wedel, A., & Hanack, M. (1999). *Synth. Met.*, 105, 145.
- [9] Newton, M. I., Starke, T. K. H., McHale, G., & Willis, M. R. (2000). *Thin Solid Films*, 360, 10.
- [10] Deng, H., Lu, Z., Shen, Y., Mao, H., & Xu, H. (1998). *Chem. Phys.*, 231, 95.
- [11] Bao, Z., Lovinger, A. J., & Brown, J. (1998). *J. Am. Chem. Soc.*, 102, 207.
- [12] Zhang, J., Wang, J., Wang, H., & Yan, D. (2004). *App. Phys. Lett.*, 84, 142.
- [13] Xiao, K., Liu, Y., Yu, G., & Zhu, D. (2003). *Appl. Phys.*, A 77, 367.
- [14] Ye, R., Baba, M., Ohishi, Y., Mori, K., & Suzuki, K. (2003). *Mol. Cryst. Liq. Cryst.*, 407, 147.
- [15] Horowitz, G. & Hajlaoui, M. E. (2000). *Adv. Mater.*, 12, 1046–1050.
- [16] Komoda, T., Endo, Y., Kyuno, K., & Toriumi, A. (2002). *Jpn. J. Appl. Phys.*, 41, 2767–2769.
- [17] Hajlaoui, M. E., Garnier, F., Hassine, L., Kouki, F., & Bouchriha, H. (2002). *Synth. Met.*, 219, 1–6.
- [18] Tsamouras, D. & Palasantzas, G. (2002). *Appl. Phys. Lett.*, 80, 4528–4530.
- [19] Sze, S. M. (1981). *Physics of Semiconductor Devices*, Wiley: New York,
- [20] Horowitz, G., Hajlaoui, M. E., & Hajlaoui, R. (2000). *J. Appl. Phys.*, 87, 4456.